

FIG. 1

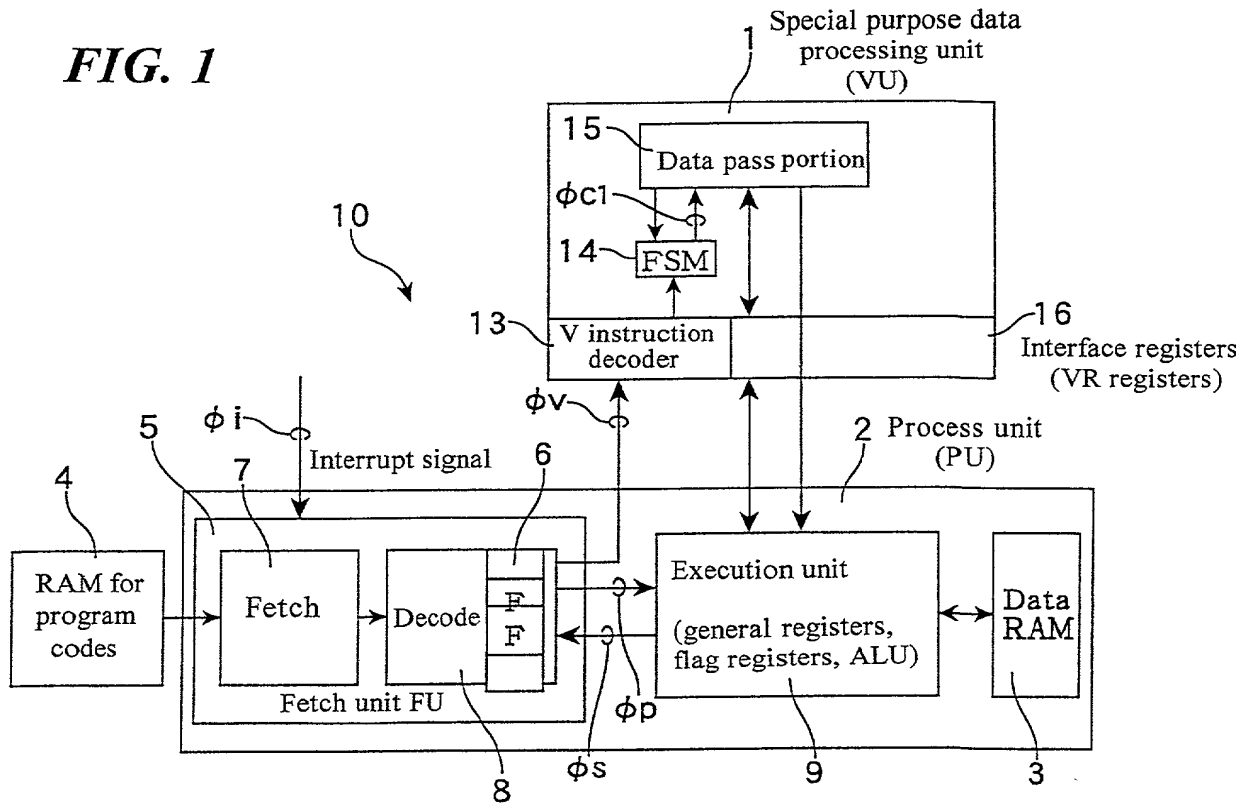


FIG. 2

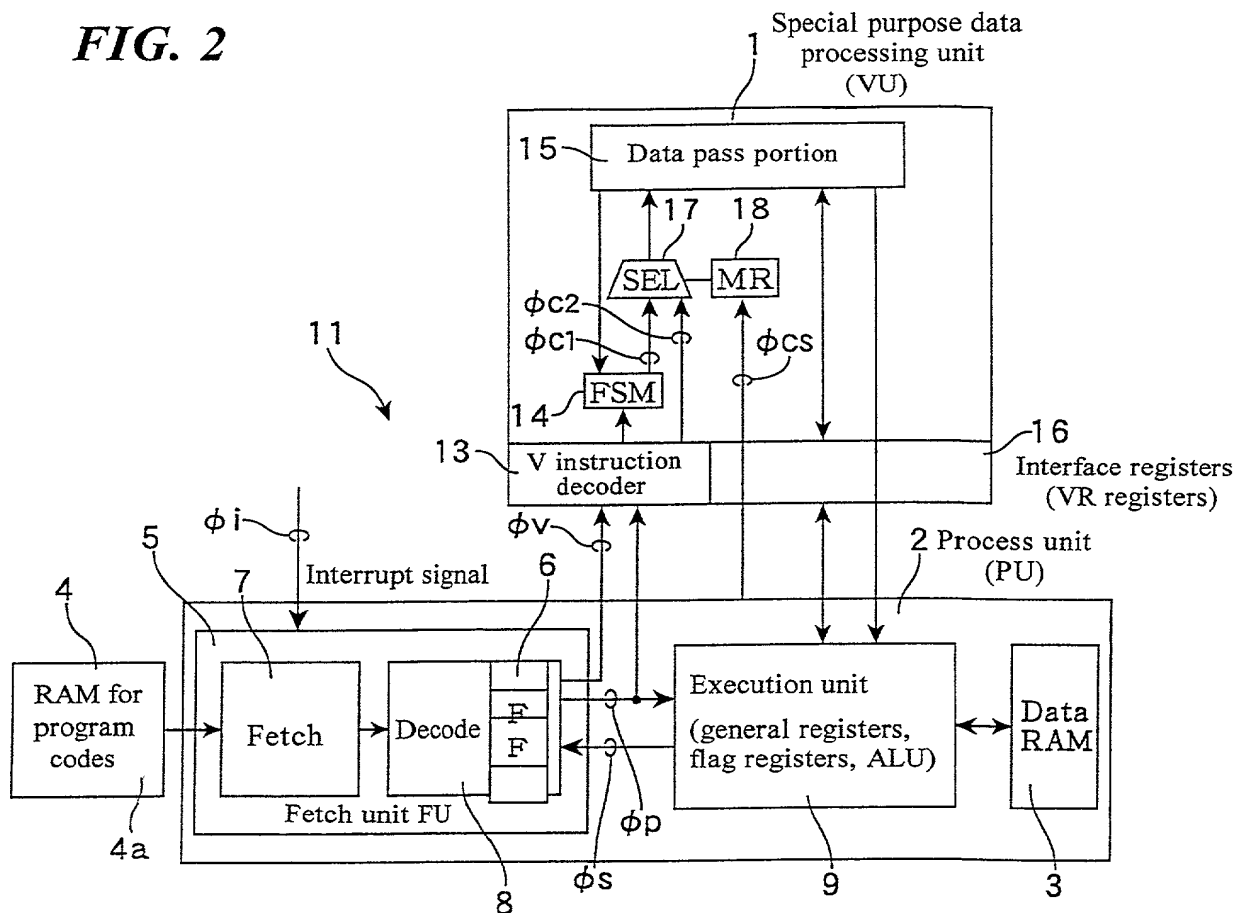


FIG. 3

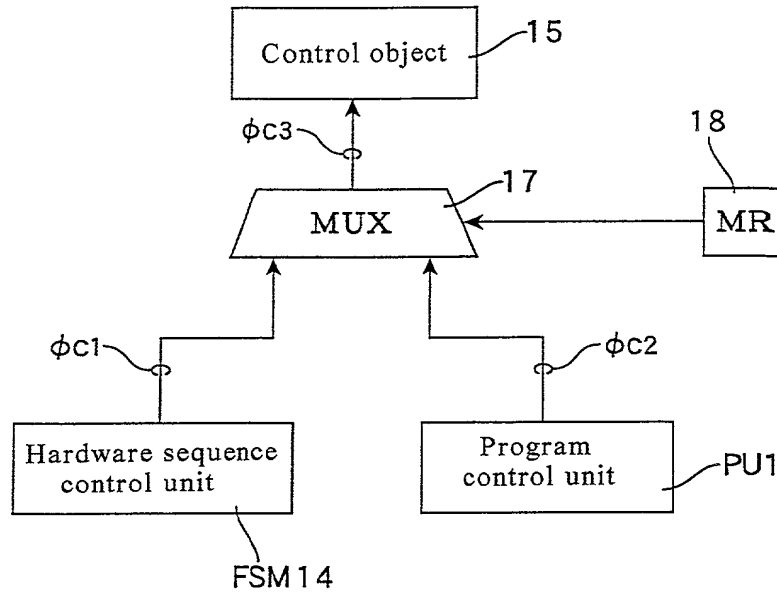


FIG. 4

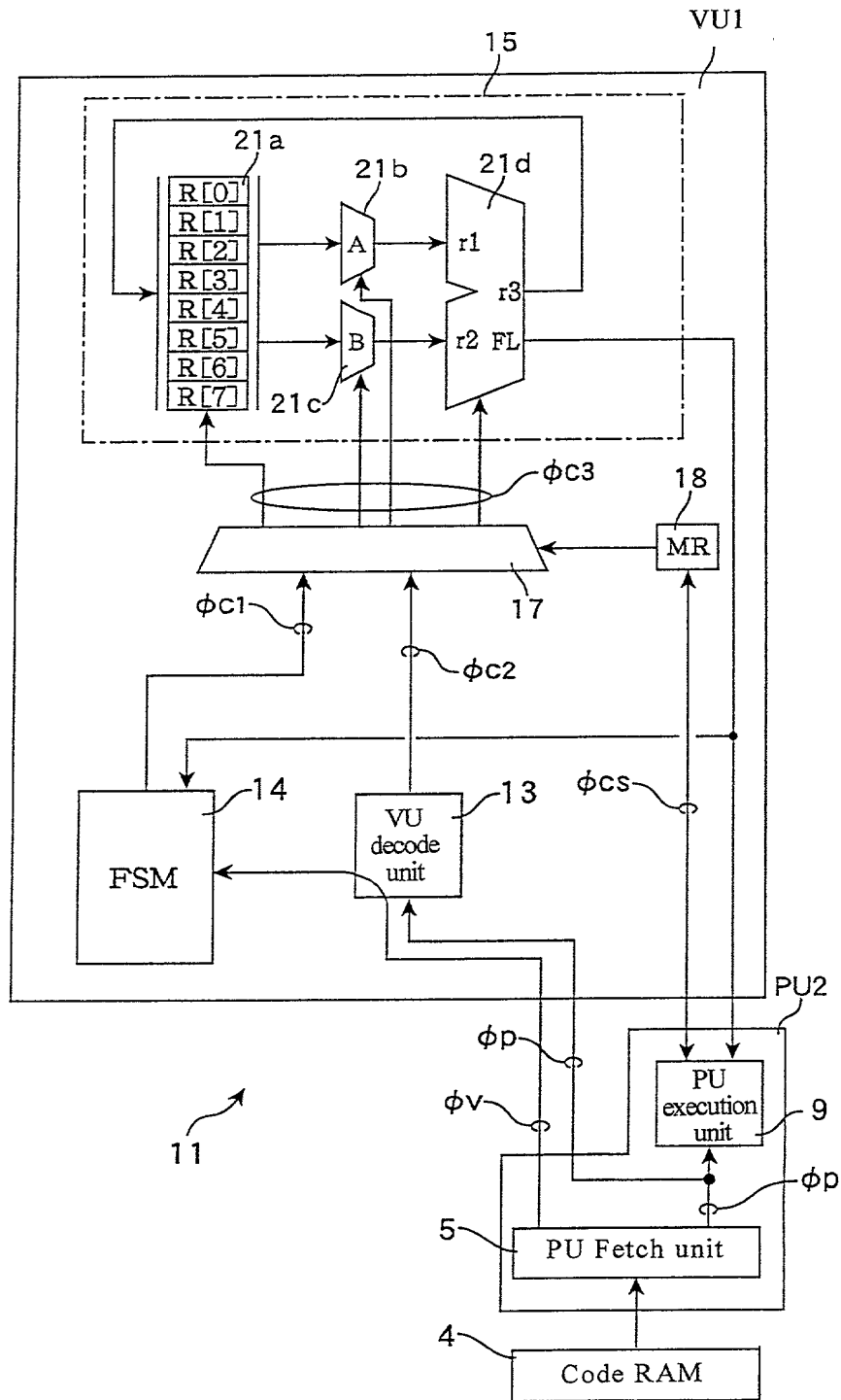


FIG. 5

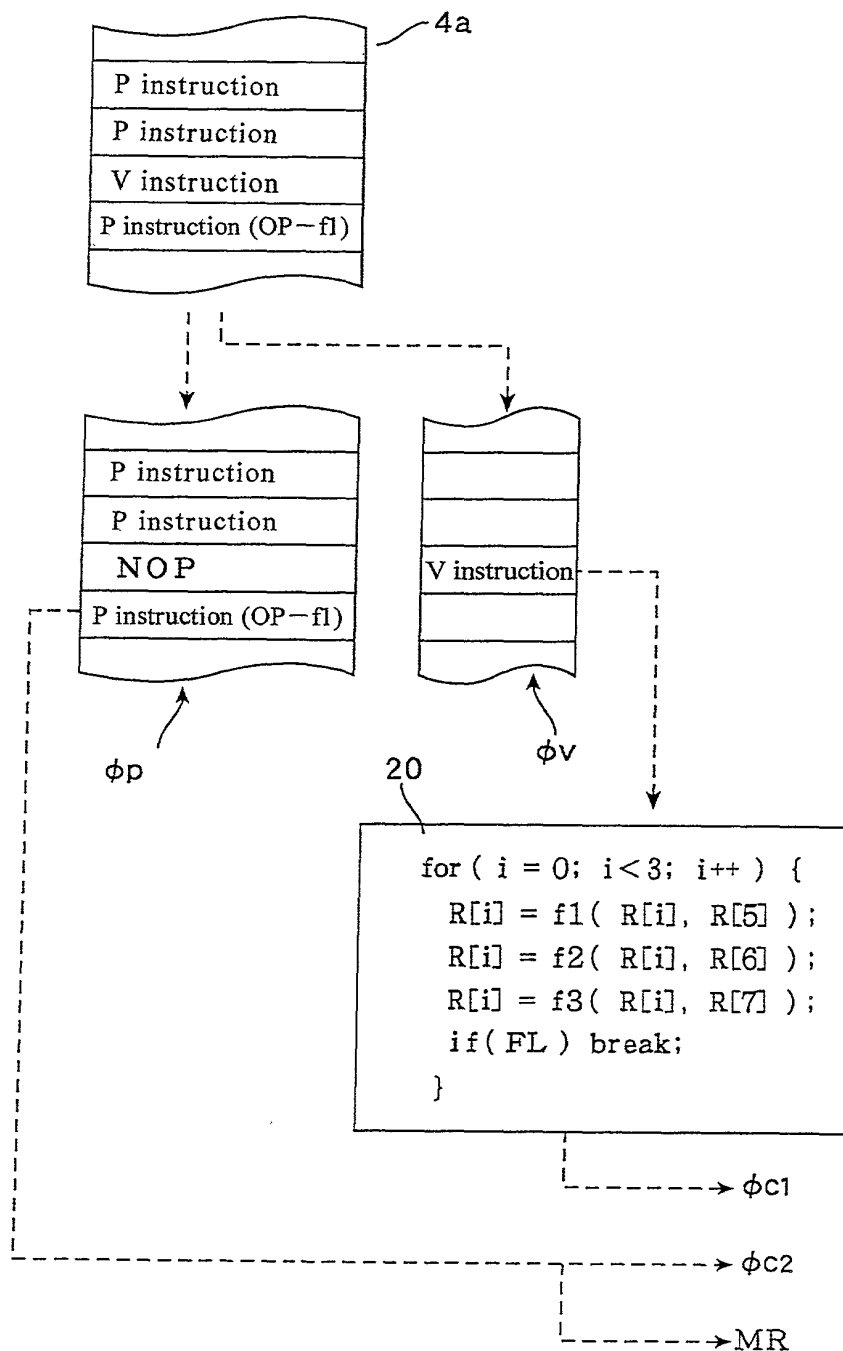


FIG. 6

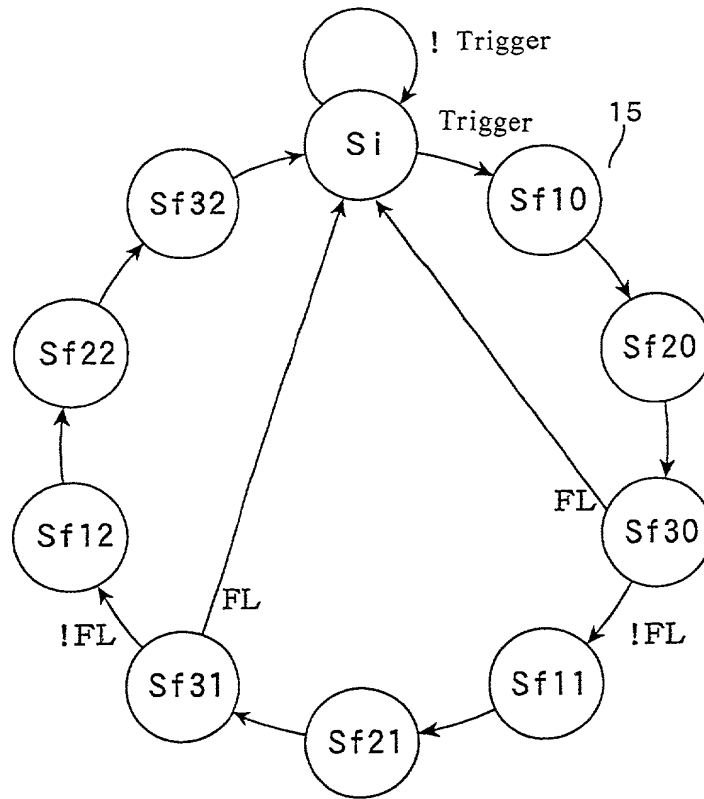


FIG. 7

$\phi C1$

Status	Register file read out selection A	Register file read out selection B	Calculation selection	Register file write selection · approve
S i	no read out	no read out	no calculation selection	no write
Sf10	R[0]	R[5]	f1	R[0]
Sf20	R[0]	R[6]	f2	R[0]
Sf30	R[0]	R[7]	f3	R[0]
Sf11	R[1]	R[5]	f1	R[1]
Sf21	R[1]	R[6]	f2	R[1]
Sf31	R[1]	R[7]	f3	R[1]
Sf12	R[2]	R[5]	f1	R[2]
Sf22	R[2]	R[6]	f2	R[2]
Sf32	R[2]	R[7]	f3	R[2]

FIG. 8

$\phi C2$

Instruction	Register file read out selection A	Register file read out selection B	Calculation selection	Register file write selection · approve
OP-f1 r3, r1, r2	r1	r2	f1	r3
OP-f2 r3, r1, r2	r1	r2	f2	r3
OP-f3 r3, r1, r2	r1	r2	f3	r3

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FIG. 9

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```
for ( i = 0; i < 3; i++ ) {  
    R[i] = f3 ( R[i], R[7] );  
    R[i] = f2 ( R[i], R[6] );  
    R[i] = f1 ( R[i], R[5] );  
    if ( FL ) break;  
}
```

FIG. 10

4b

```
TRIGGER: MOVE MR, #Program control mode  
        MOVE 1, #0  
LOOP:   CMP  1, #3  
        JRC  END      ;if(i-3<0) JMP END  
        OP-f3 R[i],R[i],R[7]  
        OP-f2 R[i],R[i],R[6]  
        OP-f1 R[i],R[i],R[5]  
        JRM  FL, END   ;if( FL ) JMP END  
        ADD  1, 1, #1  ;i=i+1  
        JMP  LOOP  
END;
```

FIG. 11

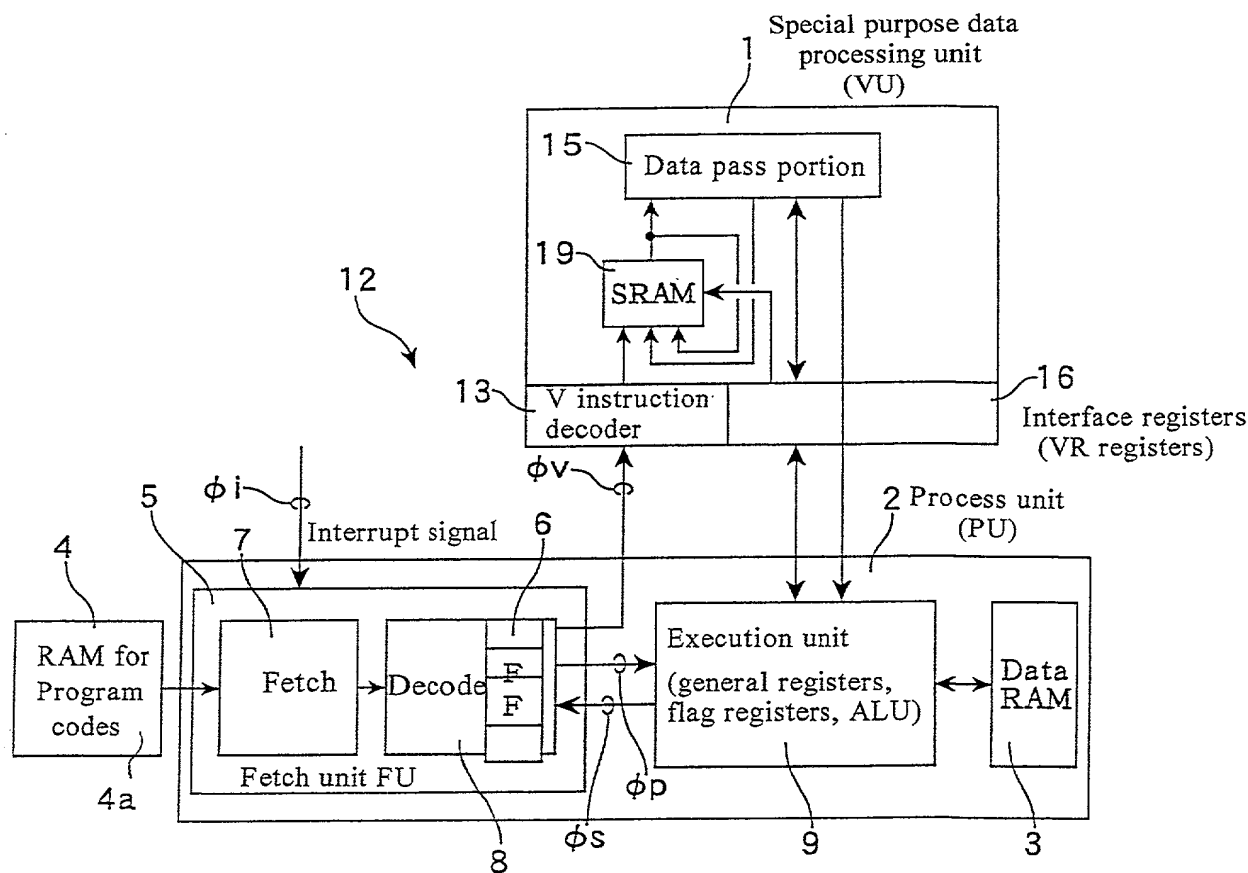


FIG. 12

